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BB60 Statement of Volatility

This document pertains to the BB60A and BB60C, all serial numbers and versions. In this document, "BB60" refers to the BB60A and the BB60C.

Non-Volatile Memory

The BB60 contains 1 or 2 SPI flash memory ICs. This non-volatile memory contains the firmware for the Cypress FX3, the temperature correction data, and 10 MHz time base correction data for the BB60.

The BB60 also contains a Xilinx Spartan 3AN FPGA with internal flash memory. This flash memory is used exclusively for the FPGA configuration file.

Non-volatile memory is modified during initial programming, as well as during calibration adjustment and firmware update procedures. Neither the Spike software nor the Application Programming Interface (API) modifies the BB60's non-volatile memory.

Volatile Memory

The Cypress FX3 and Xilinx Spartan 3AN FPGA contain volatile memory for program and data, including sweep settings and raw IF data.

Numerous ICs, including the LO synthesizers, PLLs, and ADC contain volatile configuration registers.

All volatile memory is erased when power is removed from the device.

Host PC

The Host PC running Spike software stores correction data, user presets, and recorded files to the PC's hard drive. User presets contain sensitive information such as frequencies of interest and measurement settings, and are saved by default to C:\Users\[user name]\AppData\Roaming\SignalHound. Recorded files contain sensitive information such as frequencies , amplitudes, and other characteristics of observed signals, and are saved by default to C:\Users\[user name]\Documents\SignalHound.

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